Back End:

Session AI-Sys 2: 120: Improving Realistic Worst-Case Performance of NVCiM DNN Accelerators through Training with Right-Censored Gaussian Noise

Zheyu Yan (University of Notre Dame)
Yifan Qin (University of Notre Dame)
Wujie Wen (North Carolina State University)
X. Sharon Hu (University of Notre Dame)
Yiyu Shi (University of Notre Dame)

Session HLS 2: 403: MapBuf: Simultaneous Technology Mapping and Buffer Insertion for HLS Performance Optimization

Hanyu Wang (ETH Zurich)
Carmine Rizzi (ETH Zurich)
Lana Josipovic (ETH Zurich)

Session DFM1: 577: OPT: Optimal Proposal Transfer for Efficient Yield Optimization for Analog and SRAM Circuits

Yanfang Liu (Beihang University)
Guohao Dai (Shenzhen University)
Yuanqing Cheng (Beihang University)
Wang Kang (Beihang University)
Wei Xing (Beihang University)